



Reg.No. 112813419001/RG

The Syndicate of the Anna University hereby makes known that
BHAVANI G *has been admitted to the* **DEGREE OF MASTER OF**
ENGINEERING *in* **VLSI DESIGN** *under the Faculty of Information*
and Communication Engineering, having completed the prescribed programme
of study and having been certified by the duly appointed examiners to be qualified
to receive the same, and has been placed in **FIRST CLASS** *at the Examination*
held in **JUNE 2015 .**

Given under the Seal of the University



Chennai 600 025
India
January 2016


Controller of Examinations


Registrar


Vice-Chancellor

Sl. No. 717992

Read by	<i>m. m. real</i>
Verified by	<i>V. Gandhi</i>